noestle nanga

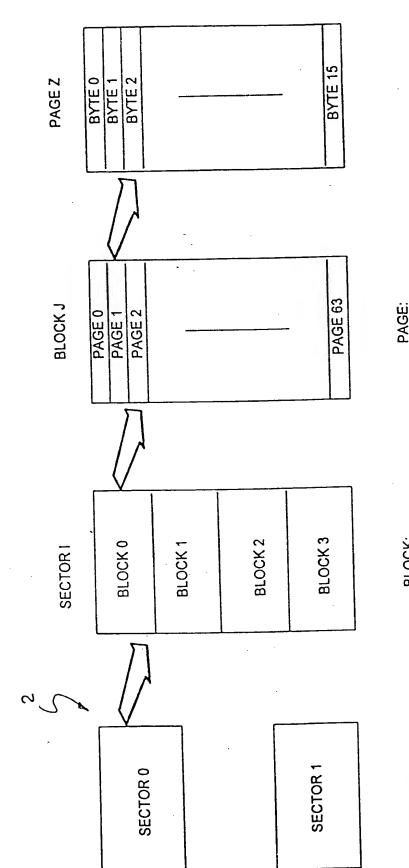


FIG. 2

PAGE: Minimal EEprom Update Area

BLOCK: Emulated EEprom Address

SECTOR:
Minimal
Erasable
Area

Simplification: 4 pages (instead of 64) for each block

on sector 0 done Erase phase 0 on sector 1 done: sector 1 ready Erase phase 3 on sector 1 done Erase phase 2 on sector 1 done Erase phase 1 on sector 1 done Erase phase 0 block 3 block 2 block 2 block 3 block 0 block 1 block 0 block 1 Eeprom sector 1 Eeprom sector 0

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224000h	FCR
224001h	ECR
224002h	FSR

FIG. 3A

Sector	Addresses	Max Size
OTP	211F80h to 211FFFh	128 byte
Flash 0	000000h to 00FFFFh	64 Kbyte
Flash 1	010000h to 01BFFFh	48 Kbyte
Flash 2	01C000h to 01DFFFh	8 Kbyte
Flash 3	01E000h to 01FFFFh	8 Kbyte
Eeprom	220000h to 2203FFh	1 Kbyte

FIG. 3B

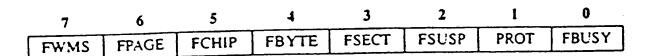


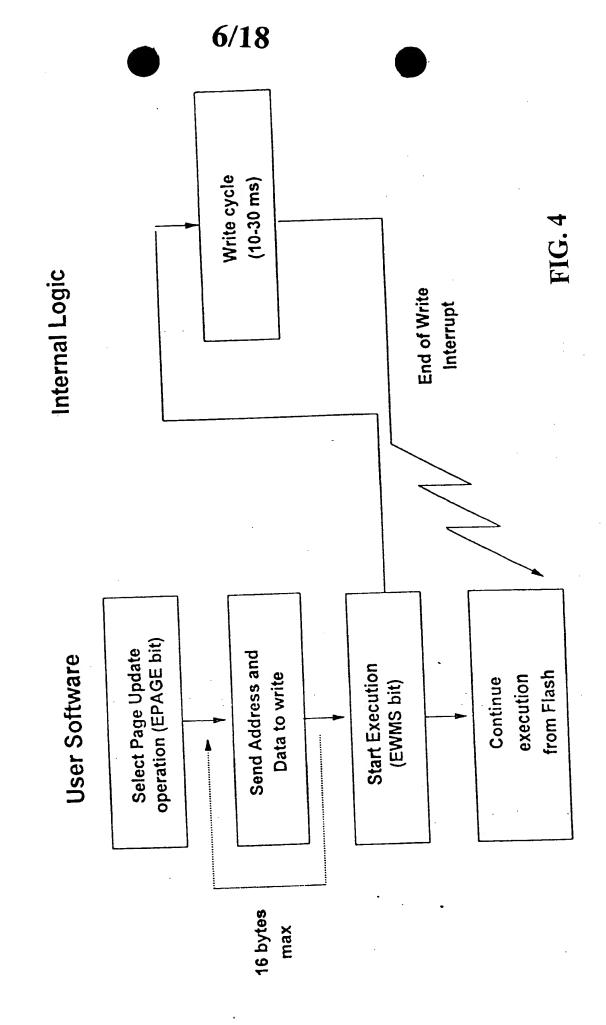
FIG. 3C

7	6	5	4	3	2	1	0	
EWMS	EPAGE	ECHIP			WFIS	FEIEN	EBUSY	

FIG. 3D

	7	6	5	4	3	2	1	0	_
ſ	FERR	FSS6	FSS5	FSS4	FSS3	FSS2	FSS1	FSS0	
- 1									

FIG. 3E



211FFCh	NVAPR
211FFDh	NVWPR
211FFEh	SVPWDO
Zilfffh	NVPWDI

FIG. 4A

Operation	Size	Min	Тур	Max
Page Update	256 byte	160 us	10 ms	30 ms
	512 byte	160 us	15 ms	50 ms
	1 Kbyte	160 us	30 ms	100 ms
Chip Erase	256 byte		35 ms	100 ms
	512 byte		45 ms	150 ms
	1 Kbyte		70 ms	300 ms

FIG. 4B

7	6	5	4	3	2	1	0
APRA	APRO	APBR	APEE	APEX	PWT2	PWTI	PWT0

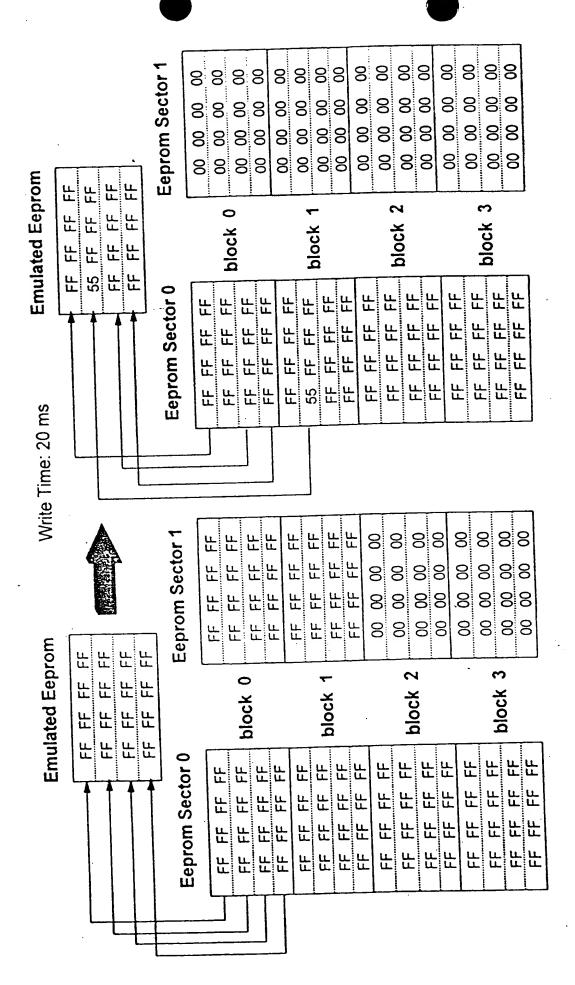
FIG. 4C

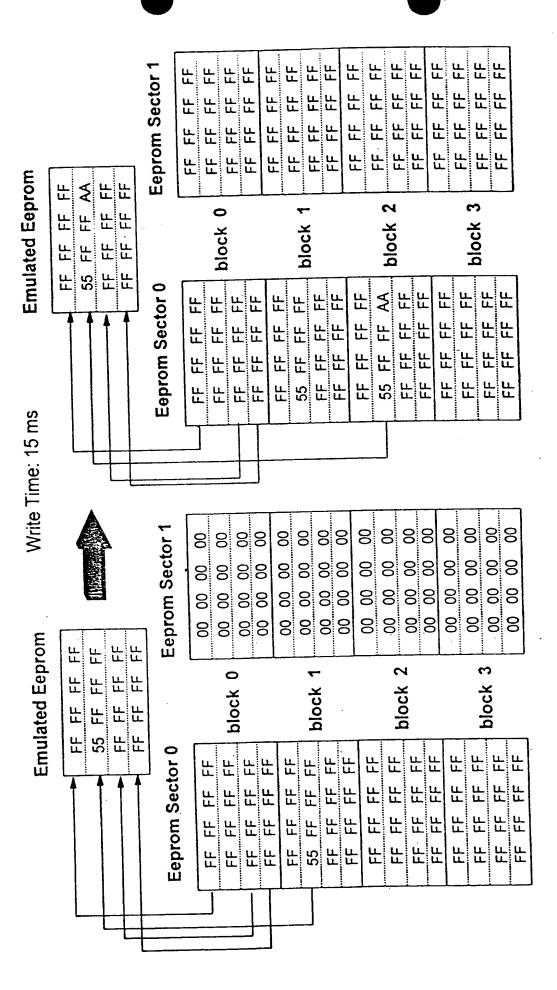
7	6	5	4	3	2	1	0
TMDIS	PWOK	WPBR	WPEE	WPRS3	WPRS2	WPRSI	WPRS0

FIG. 4D

7	٠,	6	5	4	3	2	1	0
PWI	D7	PWD6	PWD5	PWD4	PWD3	PWD2	PWDI	PWD0

FIG. 4E





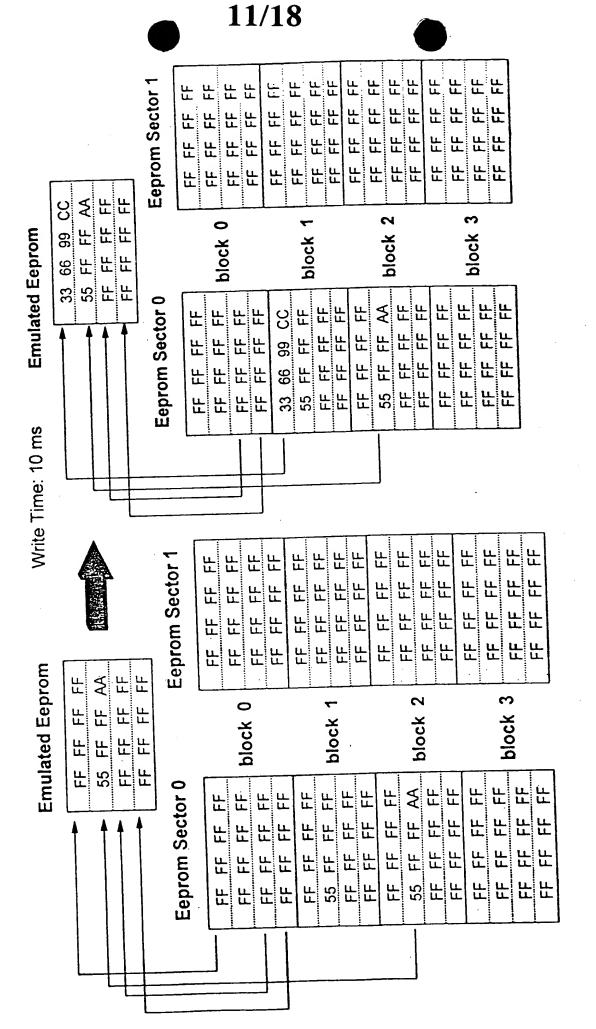


FIG. 7

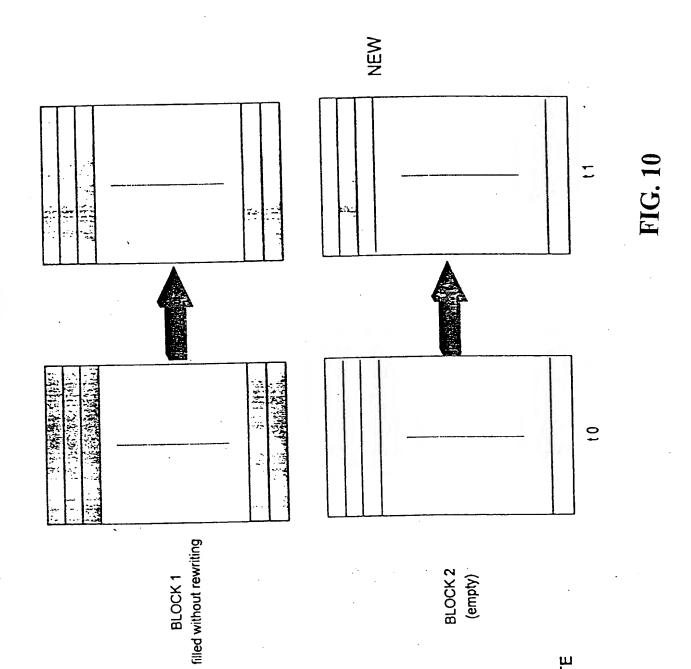
namms 1 1 4 n m n n n n n

FIG. 8

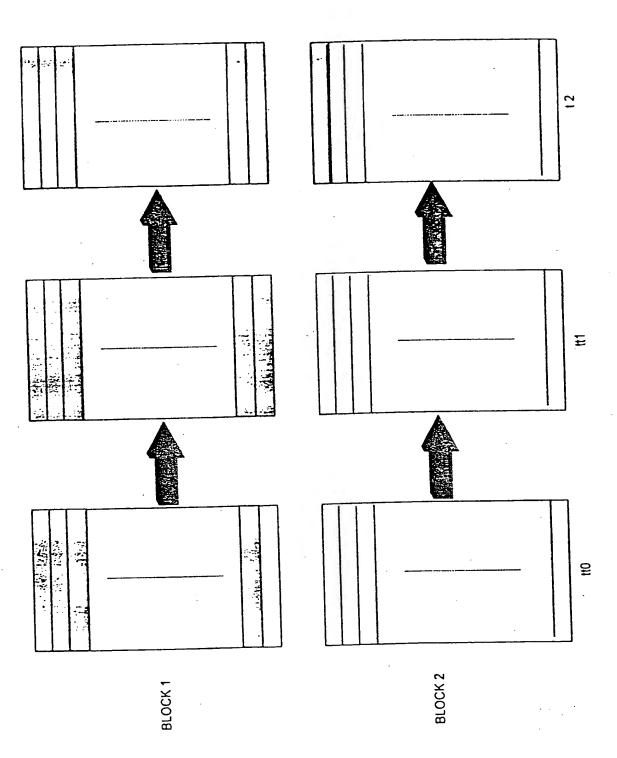
				•		
rom	CC 55 55 FF	Eeprom Sector 1	33 66 99 CC AA 44 88 55 FF FF FF			
Emulated Eeprom	33 66 99 (AA 44 88 FF FF FF		block 0	block 1	block 2	block 3
. E		Eeprom Sector 0		66 99 (FF FF FF FF	8 8 8 8	00 00 00 00 00 00
Write Time: 30 ms		Eep		33 33 47 47		8 8 8
		Eeprom Sector 1	FF	FF		FF
Emulated Eeprom	33 66 99 CC AA 44 88 AA FF FF FF FF FF FF FF		block 0	block 1	block 2	block 3
E		rom Sec	FF	33 66 99 CC 55 FF FF FF FF FF FF FF FF FF	FF FF FF FF FF FF FF FF	AA 44 88 AA FF FF FF FF FF FF FF
					<u> </u>	

FIG. 9

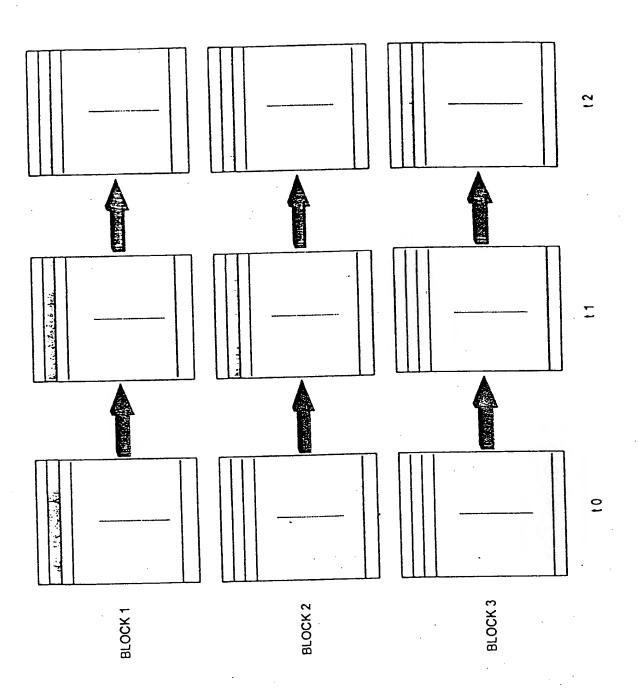




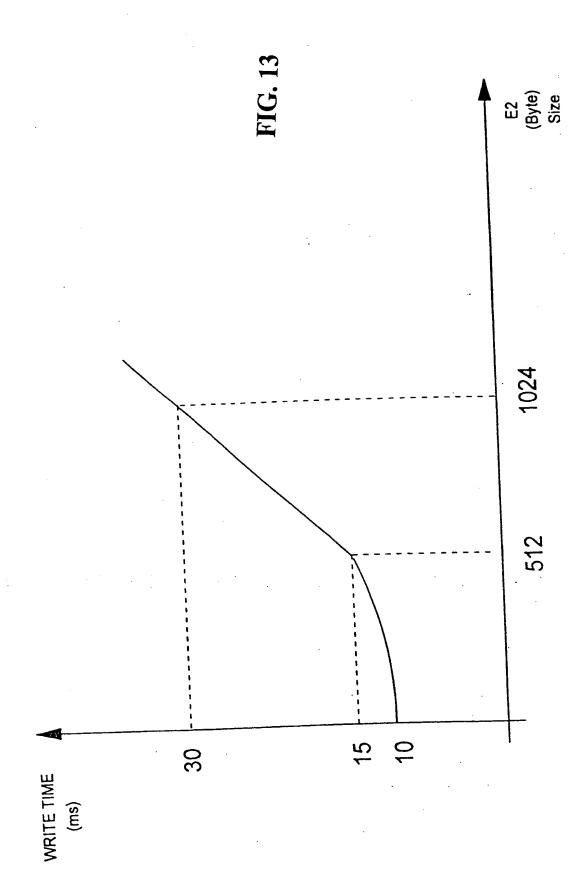
CYCLES per BYTE 100 K * 8



CYCLES per BYTE 100K * 8 / 1K * 64 = 51200



CYCLES per BYTE 100K * 8 / 1K = 800



(31) Line

